

INFORMATION DISCLOSURE CITATION PTO-1449		ATTY. DOCKET NO. P123-US		SERIAL NO. 16/698,290 Not Yet Assigned			
		APPLICANT Peter W. Richards					
		FILING DATE Herewith		GROUP Not Yet Assigned 287.3			
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
JP	US2002/0085438 A1	07/04/02	Wolverton	365	200		
	US2002/0138688 A1	09/26/02	Hsu, et al.	711	105		
	US2002/0054031 A1	05/09/02	Elliott, et al.	345	204		
	6,421,285	07/16/02	Matsuzaki, et al.	365	200		
	5,903,248	05/11/99	Irwin, et al.	345	90		
	4,638,309	01/20/87	Ott	345	84		
	6,344,672 B2	02/05/02	Huffman	257	296		
	5,132,928	07/21/92	Hayashikoshi, et al.	365	185.13		
	5,382,961	01/17/95	Gale, Jr.	345	108		
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
						<input type="checkbox"/>	<input type="checkbox"/>
						<input type="checkbox"/>	<input type="checkbox"/>
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
JP	Kompenhouwer, et al., OPTIMALLY REDUCING MOTION ARTIFACTS IN PLASMA DISPLAYS, Phillips Research Laboratories, SID 00 Digest, Pgs 388-391. YEAR 2000						
JP	Baker, et al., CMOS Circuit Design, Layout and Simulation, IEEE Press Series on Microelectronic Systems, IEEE Press, New York, Pgs 345-349. DATE N/A						

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U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
JP 	5,523,803	06/04/96	Urbanus, et al.	348	771		
	5,255,100	10/19/93	Urbanus	348	441		
	6,115,083	09/05/00	Doherty, et al.	348	771		
	6,107,979	08/22/00	Chiu, et al.	345	84		
	5,079,544	01/07/92	DeMond, et al.	345	84		
	5,548,301	08/20/96	Kornher, et al.	345	85		
	5,254,980	10/19/93	Hendrix, et al.	345	84		
	5,444,566	08/22/95	Gale, et al.	359	291		
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
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						<input type="checkbox"/>	<input type="checkbox"/>
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	<div style="border: 1px solid black; height: 100px; width: 100%; position: relative;"> <div style="position: absolute; top: 0; left: 0; right: 0; bottom: 0; border-left: 1px solid black; border-right: 1px solid black; border-bottom: 1px solid black;"></div> </div>						
EXAMINER JACK DINI		DATE CONSIDERED 10/05/04					

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.